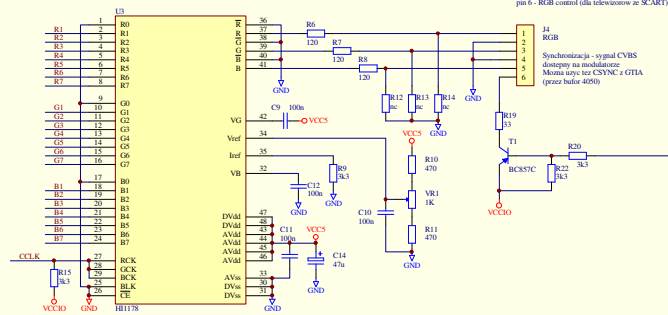
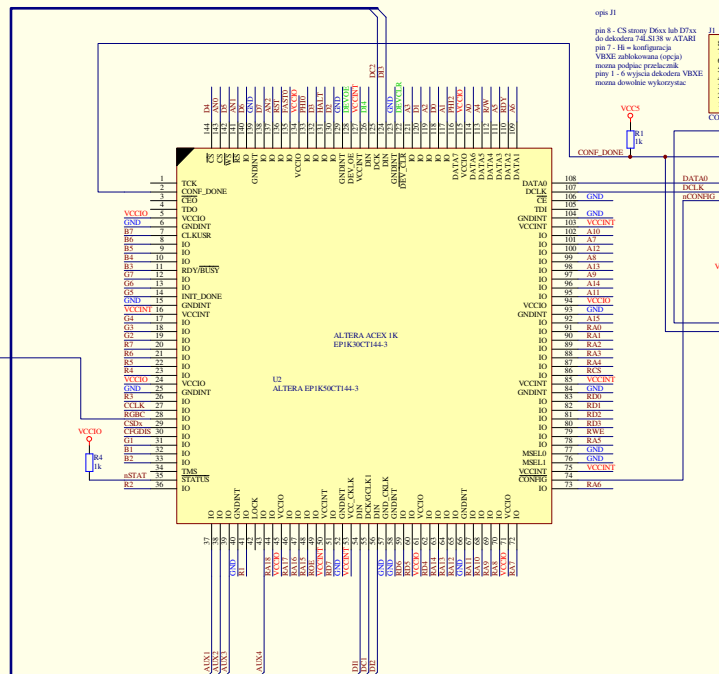


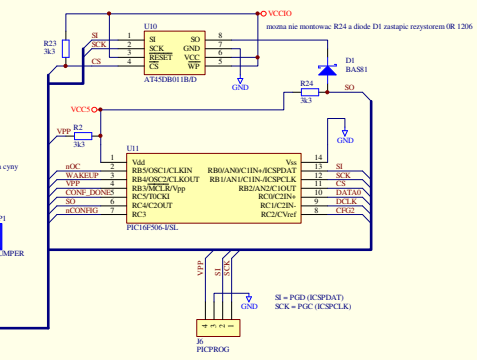
R17, R18: diodek dla regulatora LMI117-ADJ
Dla LMI117-2.5: R17 = 0R, R18 nie stosować



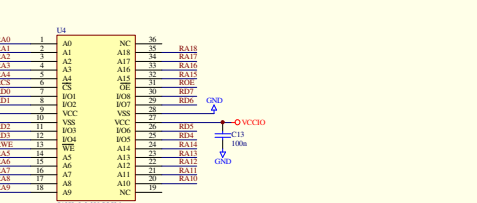
pin 1 - GND
pin 2 - GND
pin 3 - I
pin 4 - GND
pin 5 - R
pin 6 - REG control (dla telewizorów ze SCART)



pin 1 - RD Dn0 - DnB
pin 2 - RD Dn0 - DnB
pin 3 - RD Dn0 - DnB
pin 4 - WR Dn0 - DnB
pin 5 - WR Dn0 - DnB
pin 6 - WR Dn0 - DnB
pin 7 - RD Dn0 - DnB
pin 8 - WR Dn0 - DnB



pin 1 - RD Dn0 - DnB
pin 2 - RD Dn0 - DnB
pin 3 - RD Dn0 - DnB
pin 4 - WR Dn0 - DnB
pin 5 - WR Dn0 - DnB
pin 6 - WR Dn0 - DnB
pin 7 - RD Dn0 - DnB
pin 8 - WR Dn0 - DnB



pin 1 - RD Dn0 - DnB
pin 2 - RD Dn0 - DnB
pin 3 - RD Dn0 - DnB
pin 4 - WR Dn0 - DnB
pin 5 - WR Dn0 - DnB
pin 6 - WR Dn0 - DnB
pin 7 - RD Dn0 - DnB
pin 8 - WR Dn0 - DnB

Tytuł		
Nr	Number	Revision
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Date	19.09.2009	Sheet of
File	D:\U3\Programy\Elektronika\Lab	Drawn By
1	2	3
4	5	6
7	8	