



# VideoBoard XE 2.0

Original (1.0 - 1.2) design by Tomasz Plekiet, 2.0 by Sebastian Bartkowicz

Description of J1  
 pin 8 - CS (chip select) signal of D6xx or D7xx page goes to 74ALS138 pin #9 (D6xx) or pin #7 (D7xx)  
 pin 7 - H = VBXE configuration disabled (optional)  
 pins 1 - 6 outputs of VBXE decoder - can be used freely  
 pin 1 - RD Dx80 - Dx7F  
 pin 2 - RD Dx40 - Dx3F  
 pin 3 - RD Dx00 - Dx7F  
 pin 4 - WR Dx80 - Dx7F  
 pin 5 - WR Dx40 - Dx7F  
 pin 6 - WR Dx00 - Dx3F

R17 and R18 are divider network for reference voltage of LM1117-AD1. If LM1117-2.5 is used, R17 = 0R, and R18 is open